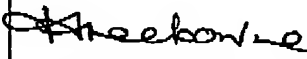


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Docket No.: CM033593

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)	Stengel, Robert E.	GROUP ART UNIT:	2634
APPLN. NO.:	10/050,233	EXAMINER:	Wong, Linda
FILED:	January 16, 2002	Confirmation No.	6380
TITLE:	DELAY LOCKED LOOP SYNTHESIZER WITH MULTIPLE OUTPUTS AND DIGITAL MODULATION		

CERTIFICATE OF FACSIMILE TRANSMITTAL	
I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, at (571) 273-8300 Centralized Facsimile and addressed to: Mail Stop: <u>Amendment</u> , Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date listed below:	
Date:	January 19, 2005
Signature: Typed or Printed Name:	 Vernice V. Freebourn

AMENDMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Office Action mailed October 5, 2005, concerning the above-identified application and is timely filed within the three month shortened statutory period for a response. Applicant submits the following Amendment and Remarks and respectfully requests the Examiner to reconsider the rejections made in the Action and to allow the claims to issue. . A Petition for Extension of Time to respond, with fee authorization, is submitted concurrently herewith.

Please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.